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APPLICATION NO.		FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/803,744		03/08/2001	Yukihisa Takeuchi	789_067	8510	
25191	7590	11/25/2003		EXAM	EXAMINER	
BURR & BROWN			KOVALICK,	KOVALICK, VINCENT E		
PO BOX 7068 SYRACUSE, NY 13261-7068		13261-7068		ART UNIT	PAPER NUMBER	
				2673	8	
			•	DATE MAILED: 11/25/2003	3	

Please find below and/or attached an Office communication concerning this application or proceeding.

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•	Application No.		Applicant(s)					
	09/803,744		TAKEUCHI ET AL					
Office Action Summary	Examiner		Art Unit					
	Vincent E Kova		2673					
The MAILING DATE of this communication appeared for Reply	pears on the cove	er sheet with the c	orrespondence ad	dress				
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication. If the period for reply specified above is less than thirty (30) days, a repl If NO period for reply is specified above, the maximum statutory period Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, how by within the statutory makes will apply and will expire, cause the application	wever, may a reply be tin inimum of thirty (30) day a SIX (6) MONTHS from to become ABANDONE	nely filed s will be considered timel the mailing date of this c D (35 U.S.C. § 133).	y. ommunication.				
Status	March 2001							
1) Responsive to communication(s) filed on <u>08</u>		final						
, <u> </u>	nis action is non-		conscition on to th	a marita ia				
 Since this application is in condition for allow closed in accordance with the practice under Disposition of Claims 				ie merits is				
4)⊠ Claim(s) <u>1-17</u> is/are pending in the application	n.							
4a) Of the above claim(s) is/are withdra		ration.						
5)⊠ Claim(s) <u>12-17</u> is/are allowed.								
6)⊠ Claim(s) <u>1,2 and 8</u> is/are rejected.	· <u>_</u>							
7)⊠ Claim(s) <u>3-7 and 9-11</u> is/are objected to.	_							
8) Claim(s) are subject to restriction and/o	or election require	ement.						
Application Papers								
9) The specification is objected to by the Examine		stad to builbo Evo	min or					
10) The drawing(s) filed on is/are: a) acce		·						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). 11) The proposed drawing correction filed on is: a) approved b) disapproved by the Examiner.								
If approved, corrected drawings are required in reply to this Office action.								
12) The oath or declaration is objected to by the Examiner.								
Priority under 35 U.S.C. §§ 119 and 120								
13) Acknowledgment is made of a claim for foreig	n priority under 3	35 U.S.C. § 119(a)-(d) or (f).					
a)⊠ All b)□ Some * c)□ None of:		,	~					
1. Certified copies of the priority document	ts have been rec	eived.						
2. Certified copies of the priority document								
 3. Copies of the certified copies of the prio application from the International But * See the attached detailed Office action for a list 	ıreau (PCT Rule	17.2(a)).		Stage				
14) Acknowledgment is made of a claim for domest	ic priority under	35 U.S.C. § 119(e	e) (to a provisiona	l application).				
a) The translation of the foreign language pro	* *							
Attachment(s)	, , ,							
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s) 4	4) 5) 5,6 <u>8.7</u> . 6)	Notice of Informal I	v (PTO-413) Paper No Patent Application (PT					

Art Unit: 2673

DETAILED ACTION

1. This Office Action is in response to Applicant's Patent Application, Serial No. 09/803,744, with a File Date of March 8, 2001.

Claim Rejections - 35 USC § 103

- 2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. Claims 1-2 are rejected under 35 U.S.C. 103(a) as being unpatentable over Shiota et al. (USP 6,288,756) taken with Matsuura (USP 6,493,468).

Relative to claim 1, Shiota et al. **teaches** luminance correction circuit and video display monitor thereof (col. 2, lines 18-34); Shiota et al. further **teaches** a method for producing a display apparatus constructed by arranging a plurality of display units arranged with a plurality of display components, said method comprising the steps of displaying a uniform image on said display apparatus to detect luminances of said respective display components (col. 3, lines 7-14); still further, Shiota et al. **teaches** calculating luminance correction coefficients for said respective display components on the basis of luminance target values of said respective display components (col. 3, lines 7-14).

Shiota et al. **does not teach** calculating luminance target values of said respective display components.

Art Unit: 2673

Matsuura teaches an image processing apparatus and method (col. 1, lines 64-67 and col. 2, lines 1-61); Matsuura further **teaches** calculating luminance target values of said respective display components (col. 2, lines 16-26).

It would have been obvious to a person of ordinary skill in the art at the time of the invention to incorporate in the methodology as taught by Shiota et al. the feature as taught by Matsuura in order to establish the luminance target values necessary for correcting unevenness (smoothing) in the luminance level and color of video images displayed on video display monitors as suggested by Shiota et al. (col. 2, lines 1-14).

Regarding claim 2, Matsuura further **teaches** the method step for producing said display apparatus wherein said luminance target value is calculated by averaging said luminances of said display component and said plurality of display components arranged therearound, and regarding an obtained average value as said luminance target value of said display component (col. 2, lines 16-26).

4. Claim 8 is rejected under 35 U.S.C. 103(a) as being unpatentable over Shiota et al. taken with Matsuura as applied to claim 1 in item 3 hereinabove, and further in view of Kasahara et al. (USP 6,509,884).

Regarding claim 8, Shiota et al. taken with Matsuura **does not teach** the method of producing said display apparatus wherein said luminance correction coefficient is calculate in consideration of color temperature.

Kasahara et al. **teaches** a display device and luminance control method therefor (col. 2, lines 11-67 and col. 3, lines 1-18); Kasahara et al. further **teaches** a method of producing said display

Art Unit: 2673

apparatus wherein said luminance correction coefficient is calculate in consideration of color temperature (col. 1, lines 34-41).

It would have been obvious to a person of ordinary skill in the art at the time of the invention to incorporate in the methodology as taught by Shiota et al. taken with Matsuura the feature as taught by Kasahara et al. in order to put in place the means to monitor the thermal stresses created by the temperature difference in the display screen and control the luminance accordingly.

Allowable Subject Matter

5. Claims 3-7 and 9-11 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Relative to claim 3, the prior art of recored (USP 6,509,884, Kasahara et al.; USP 6,493,468, Matsuura and USP 6,288,756, Shiota et al.) **does not teach** the method for producing said display apparatus wherein said plurality of display components, which are arranged around said display component, are included in a group of said display components corresponding to (2m + 1) rows aligned in a vertical direction, and they are included in a group of said display components corresponding to (2n + 1) columns aligned in a horizontal direction.

Regarding claim 6, the said prior art of record **does not teach** the method for producing said display apparatus further comprising: a first step of retrieving said display component which

display apparatus further comprising: a first step of retrieving said display component which exhibits a minimum value of said calculated luminance target values; and a second step of increasing said current luminance target value by a certain value for said retrieved display

Art Unit: 2673

component.

As to claim 7, the said prior art of record **does not teach** the method for producing said display apparatus further comprising the steps of: retrieving said display component which exceeds a threshold value of said calculated luminance target values; and decreasing said current luminance target value to said threshold value for said retrieved display component.

Regarding claim 9, the said prior art of record **does not teach** the method for producing said display apparatus further comprising the steps of: INIP performing standardization for said calculated luminance target values for said respective display components in accordance with a color scheme respectively; making amendment so that values obtained after said standardization are included in a certain range; and performing a restoring process for values obtained after said amendment in accordance with said color scheme respectively to obtain luminance target values in consideration of said color temperature.

Relative to claim 11, the said prior art of record **does not teach** the method for producing said display apparatus wherein said display unit is a display unit comprising an optical waveguide plate for introducing light from a light source thereinto, and a driving section provided opposingly to a first plate surface of said optical waveguide plate and arranged with said display components of a number corresponding to a large number of picture elements, wherein a screen image corresponding to an image signal is displayed on said optical waveguide plate by controlling a displacement action of an actuator element of said display component in a direction to make contact or separation with respect to said optical waveguide plate in accordance with an attribute of said image signal to be inputted so that leakage light is controlled at a predetermined portion of said optical waveguide plate.

Art Unit: 2673

- 6. Claims 12-17 are allowed.
- 7. The following is an examiner's statement of reasons for allowance:

Relative to claim 12, the major difference between the teachings of the said prior art of record and that of the instant invention is that said prior art **does not teach** a method for producing a display apparatus constructed by arranging a plurality of display units arranged with a plurality of display components, said method comprising the steps of obtaining characteristic values for said respective display units respectively; ranking said display units on the basis of said obtained characteristic values; partitioning an arrangement area (Z10) for said plurality of display units of said display apparatus to designate ranks of said display units to be arranged in respective areas (Z11, Z12); and arranging said display units in accordance with said designation to manufacture said display apparatus.

Conclusion

8. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

U. S. Patent No.	6,388,648	Clifton et al.
U. S. Patent No.	6,219,460	Tatsuta
U. S. Patent No.	6,091,397	Lee
U. S. Patent No.	6,020,868	Greene et al.
U. S. Patent No.	5,774,101	Hirai et al.
U. S. Patent No.	5,668,569	Green et al.
U. S. Patent No.	5,106,181	Rockwell, III

Art Unit: 2673

Responses

9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Vincent E Kovalick whose telephone number is 703 306-3020. The examiner can normally be reached on Monday-Thursday 7:30- 4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin Shalwala can be reached on 703 305-4938. The fax phone numbers for the organization where this application or proceeding is assigned are 703 872-9314 for regular communications and 703 872-9314 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703 306-0377.

Vincent E. Kovalick

Moralick

May 29, 2003